



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 09/556,491  
Filing Date .... April 21, 2000  
Inventor .... Scott E. Moore et al.  
Assignee .... Micron Technology, Inc.  
Group Art Unit .... 3723  
Examiner .... D. Thomas  
Attorney's Docket No. .... MI22-1422  
Title: "Semiconductor Workpiece Processing Methods"

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**


References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. No admission is made regarding whether all the submitted references are prior art.

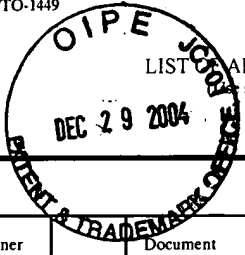
Respectfully submitted,

Dated: 12/28/04

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Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-1422	SERIAL NO. 09/556,491			
 LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Scott E. Moore et al.				
				FILING DATE April 21, 2000	GROUP 3723			
U.S. PATENT DOCUMENTS								
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate		
	AA	5,446,531	8/29/1995	Boyer et al.				
	AB	4,730,922	3/15/1988	Bach et al.				
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
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	AJ							
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	AO							
	AP							
	AQ							
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FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AR							
	AS							
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
	AT							
EXAMINER				DATE CONSIDERED				
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